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... and in the limited range of the **PLL's phase comparator** ... number count N provided by an N counter (**second counter**). ... an N counter 16, a magnitude **comparator** 17, an F ...  
[www.bandwidthmarket.com/resources/patents/data21/5121417.html](http://www.bandwidthmarket.com/resources/patents/data21/5121417.html) - 58k - Supplemental Result -

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... is fed to the input of a **second counter** 64 ... Output signal U/D of the **comparator** indicates the relative ... Machine 70 receives the **PLL** Reset signal and the external ...  
[www.bandwidthmarket.com/resources/patents/data34/5257294.html](http://www.bandwidthmarket.com/resources/patents/data34/5257294.html) - 80k - Supplemental Result -

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... generating circuit 3A includes a **phase locked loop (PLL)** 3c, a ... edge detecting circuit 3m, a **second counter** circuit 3n ... amplifier AP1, and a **comparator** (a voltage ...  
[www.bandwidthmarket.com/resources/patents/apps/2001/12/20010048281.html](http://www.bandwidthmarket.com/resources/patents/apps/2001/12/20010048281.html) - 67k - Supplemental Result -

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... for decoding an output from said **second counter**; a third ... comparison output from said third phase **comparator** circuit; a ... [0003] A **PLL (Phase-Locked Loop)** or a ...  
[www.bandwidthmarket.com/resources/patents/apps/2001/10/20010026179.html](http://www.bandwidthmarket.com/resources/patents/apps/2001/10/20010026179.html) - 101k - Supplemental Result -

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#### Patent 5280499: Spread spectrum communication system

... of the SS communication system with no **PLL** circuit being ... e) shows the output signal of the **second counter** 24. ... 12, the **comparator** 21 and the counter 22 as shown ...

[www.freepatentsonline.com/5280499.html](http://www.freepatentsonline.com/5280499.html) - 62k - [Cached](#) - [Similar pages](#)

#### Patent 4527261: Hiline interference eliminator

... to zero, said first and **second counter** means retaining ... lead 72 to an inverting **comparator** circuit 74 ... of a CD4046 Micropower **Phase-Locked Loop** circuit described ...

[www.freepatentsonline.com/4527261.html](http://www.freepatentsonline.com/4527261.html) - 52k - [Cached](#) - [Similar pages](#)

#### Recording/reproducing apparatus

... SCR or PCR inputted to **PLL** is set ... reference numeral 204 represents a **comparator**, reference numeral 205 represents a **second counter**, reference numeral ...

[patdb.ffii.org/EP/7/1/EP712256.html](http://patdb.ffii.org/EP/7/1/EP712256.html) - 101k - [Cached](#) - [Similar pages](#)



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Electronics Letters, Volume: 39, Issue: 14, 10 July 2003

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